

S.B. Roll No.....

MICROPROCESSOR

4TH Exam/ECE/ETV/COMP/IT/CSE/EEE/ECE(II)/6129/2565/0624/May'17

Duration: 3Hrs

M. Marks: 75

SECTION A

Q1. Do as directed:

10x1.5=15

- a. 8085 has _____ address lines.
- b. Stack Pointer (SP) points to the next instruction to be executed. (T/F)
- c. Accumulator (A) in 8085 is _____ bit register.
- d. SID pin stands for _____ in 8085.
- e. _____ is lowest priority interrupt.
- f. ALE stands for _____
- g. 8257 is a _____ controller.
- h. _____ and _____ instructions are used for input/ output data operations.
- i. 8251 is a programmable peripheral interface (PPI). (T/F).
- j. 8085 microprocessor contains _____ flags.

SECTION- B

Q2: Attempt any five questions.

5x6=30

- i. Explain PUSH and POP instructions.
- ii. Explain the concept of DMA data transfer.
- iii. What do understand by interrupt priority? Explain.
- iv. What is a system bus? Explain various types of buses.
- v. Explain flag register of 8085.
- vi. Differentiate between synchronous and asynchronous data transfer techniques.
- vii. Explain the instruction format of 8085.

SECTION-C

Attempt any three questions.

3x10=30

- Q3.** Explain various processor control instructions of 8085..
- Q4.** Draw the internal architecture of 8085 microprocessor and explain it.
- Q5.** Explain the function of 8279 keyboard/ display interface chip.
- Q6.** What is interrupt driven data transfer? Explain in detail.
- Q7.** Write note on any two:
- a. Instruction Cycle
 - b. Stack
 - c. EEPROM